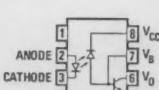
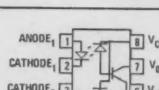
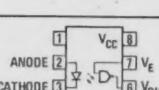
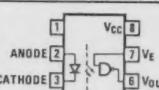
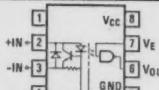
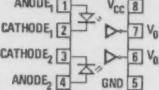
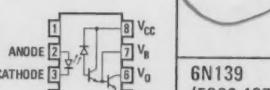
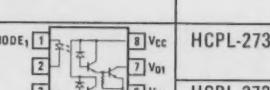
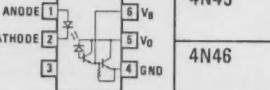


High Speed Optocouplers

Device	Description	Application ^[1]	Typical Data Rate (NRZ)	Current Transfer Ratio	Specified Input Current	Input To Output Insulation	Page No.
 6N135 (5082-4350) 6N136 (5082-4351) HCPL-2502 (5082-4352)	Transistor Output	Line Receiver, Analog Circuits, TTL/CMOS, TTL/LSTTL Ground Isolation	1M bit/s	7% Min.	16mA	3000Vdc ^[3]	186
				19% Min.			
				15-22% ^[2]			
 HCPL-2530 (5082-4354) HCPL-2531 (5082-4355)	Dual Channel Transistor Output	Line Receiver, Analog Circuits, TTL/CMOS, TTL/LSTTL Ground Isolation	1M bit/s	7% Min.	16mA	3000Vdc ^[3]	190
				19% Min.			
 6N137 (5082-4360)	Optically Coupled Logic Gate	Line Receiver, High Speed Logic Ground Isolation	10M bit/s	700% Typ.	5.0mA	3000Vdc ^[3]	194
 HCPL-2601 (5082-4361)	High Common Mode Rejection, Optically Coupled Logic Gate	Line Receiver, High Speed Logic Ground Isolation In High Ground or Induced Noise Environments	10M bit/s	700% Typ.	5.0mA	3000Vdc ^[3]	198
 HCPL-2602	Optically Coupled Line Receiver	Replace Conventional Line Receivers In High Ground or Induced Noise Environments	10M bit/s	700% Typ.	5.0mA	3000Vdc ^[3]	202
 HCPL-2630 (5082-4364)	Dual Channel Optically Coupled Gate	Line Receiver, High Speed Logic Ground Isolation	10M bit/s	700% Typ.	5.0mA	3000Vdc ^[3]	208

Low Input Current/High Gain Optocouplers

Device	Description	Application ^[1]	Typical Data Rate (NRZ)	Current Transfer Ratio	Specified Input Current	Input To Output Insulation	Page No.
 6N138 (5082-4370) 6N139 (5082-4371)	Low Saturation Voltage, High Gain Output, $V_{CC}=7V$ Max.	Line Receiver, Low Current Ground Isolation, TTL/TTL, LSTTL/TTL, CMOS/TTL	300k bit/s	300% Min.	1.6mA	3000Vdc ^[3]	212
				400% Min.			
 HCPL-2730 HCPL-2731	Dual Channel, High Gain, $V_{CC}=7V$ Max.	Line Receiver, Polarity Sensing, Low Current Ground Isolation	300k bit/s	300% Min.	1.6mA	3000Vdc ^[3]	216
				400% Min.			
 4N45 4N46	Darlington Output $V_{CC}=7V$ Max.	AC Isolation, Relay-Logic Isolation	3k bit/s	250% Min.	1.0mA	3000Vdc ^[3]	220
				350% Min.			